



SMC3 HR high reliability resistors

ESA SCC 4001

- surface mount
- metal film technology



Originally developed for automatic placement equipment these surface mount resistors have excellent overall performances.

These components are manufactured, selected and tested to ESA SCC 4001 specification.

As defined in the ESA SCC specification two test levels are proposed:

- level B with serialized components,
- level C without serialization.

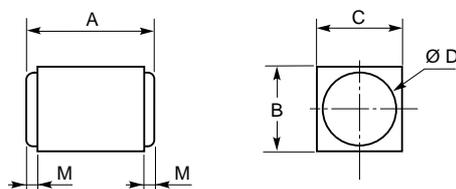


Table 1

Dimensions	min.	max.
A	3,10	3,30
B	1,50	1,70
C	1,50	1,70
D	1,40	1,45
M	0,45	0,65
Unit max. weight: 0,03 g		

Dimensions in mm

Fig. 1

SPECIFICATIONS

Table 2

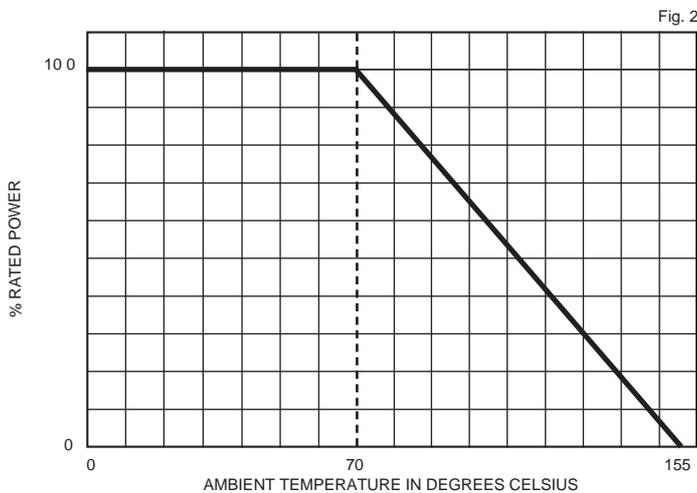
SFERNICE DESIGNATION	SMC3 HR
ESA SPECIFICATION APPLIED	4001/021
POWER RATING AT +70°C	0,25 W
LIMITING ELEMENT VOLTAGE	250 V
OHMIC VALUES VALUES ± 25 ppm/°C VERSUS TEMPERATURE ± 50 ppm/°C COEFFICIENT	49,9 Ω to 511 kΩ 1 Ω to 1 MΩ
TOLERANCE	± 0,1 % to ±1%
TEMPERATURE RANGE	–55°C ... +155°C

PERFORMANCES

Table 3

TESTS	CONDITIONS	LIMIT DRIFTS
INSULATION RESISTANCE	CEI 115 - 1 $U_T = 100\text{ V}$	1 GΩ min.
SOLDERING (thermal shocks)	CEI 68-2-20A test Tb $T = +260^\circ\text{C}$ $t \geq 10\text{ s}$	$\pm (0,15 + \frac{0,05\ \Omega}{R} \times 100)\%$
TERMINAL STRENGTH	CEI 115 - 1 adhesive test	$\pm (0,25 + \frac{0,05\ \Omega}{R} \times 100)\%$
CLIMATIC SEQUENCE	SCC 4001 and CEI 68-2- ..., 95% R.H., $+40^\circ\text{C}$ $-55^\circ\text{C} / +155^\circ\text{C} / 6\text{ cycles}$	$\pm (0,50 + \frac{0,05\ \Omega}{R} \times 100)\%$ Insulation resistance > 100 MΩ
LOAD LIFE	2000 h at rated power at $+70^\circ\text{C}$, 90' / 30' cycle	$\pm (0,50 + \frac{0,05\ \Omega}{R} \times 100)\%$ Insulation resistance > 1000 MΩ
HIGH TEMPERATURE EXPOSURE	2000 h no load at $+155^\circ\text{C}$	$\pm (0,50 + \frac{0,05\ \Omega}{R} \times 100)\%$ Insulation resistance > 1000 MΩ

POWER RATING CHART



MARKING

The ohmic value is shown with a four digit code, the first three digits are significant figures, the fourth digit specifies the number of zero to follow.
The tolerance (letter code): B : ± 0,1 %, D: ± 0,5%, F: ± 1%, G: ± 2%, J: ± 5%; the temperature coefficient (1 digit): 9 for 30 ppm/°C, 2 for: ± 25 ppm/°C and 3 for : ± 50 ppm/°C.

PACKAGING

Resistors are packaged in transparent blister pack for resistors tested to level B and in wafer pack for resistor tested to level C.
Both packages contain 80 resistors.
On the blister, information printed is: SFERNICE designation, ESA/ SCC detail specification, ohmic value, tolerance and manufacturing date code (4 digits), two for the year and two for the week.

EXTENDED RANGE

Please, consult SFERNICE for ohmic values, tolerances and also temperature coefficient (as ±15 ppm/°C) outside the qualified range.

ORDERING PROCEDURE

